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CLMPTO

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1. (Amended) A chip card [(CA, SIM) able to receive] ~~that receives~~ compressed data fields [(DATA)] each preceded by an indication of the expected length of decompressed data [(LDD)] and a length of compressed data [(LC)], is characterised in that it comprises a first means (MC) ~~comprising a first memory~~ for storing the received fields of compressed data [(DATA, TR)] according to the lengths of the respective compressed data [(LC)], a second means (MS) ~~a second memory~~ for storing a decompression algorithm, a [means (PR)] ~~processor~~ for decompressing, according to [the] said decompression algorithm, the compressed data in each field [and] into decompressed data over a length depending on the indication of length of decompressed data [(LDD)], and a third [means (MD)] ~~memory~~ for storing the decompressed data.

2. (Amended) A chip card according to Claim 1, in which the second [storage means (MS)] ~~memory~~ contains several decompression algorithms [(AL0 to ALi)], and the [decompression means (PR)] ~~processor~~ detects a decompression algorithm number [(ALi)] preceding each received field of compressed data so that [these are] ~~the compressed data is~~ decompressed in accordance with the decompression algorithm whose number has been detected.

3. (Amended) A chip card according to Claim 2, in which the second [storage means (MS)] ~~memory~~ comprises several decompression models [(M0) to MU)] respectively

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associated with the decompression algorithms [(A10 to A1J)], and the [decompression means (PR)] processor detects a decompression model number [(Mij)] preceding each received field of compressed data so that [these are] the compressed data is decompressed in accordance with the corresponding decompression algorithm and decompression model whose numbers have been detected.

4. (Amended) A chip card according to Claim 2, comprising a fourth [means (MM)] memory for storing a decompression model received previously to a received compressed data field, and [the decompression means (PR)] wherein said processor detects the number of a decompression algorithm [(A10)] preceding [the] said received field of compressed data so that [these are] the compressed data is decompressed according to the decompression algorithm whose number has been detected and the decompression model [read] stored in the fourth storage means (MM)] memory.

5. (Amended) A chip card according to Claim 2, comprising a fourth [means (MM)] memory for storing a decompression model implicitly deduced from a compressed data field during writing in the first [storage means (MC)] memory, and the [decompression means (PR)] processor detects the number of a decompression algorithm [(A10)] preceding [the] said stored field of compressed data so that [these are] the compressed data is decompressed according to the decompression algorithm whose number was detected and

the derived decompression model [read] stored in the fourth [storage means (MM)]

memory.

6. (Amended) A chip card according to [any one of Claims 1 to 5, in which the decompression means (PR)] claim 1, wherein said processor detects an indication [(CLA: B2, B3) on] of the compressed or non-compressed state of each received [compressed] data field so that the [decompression means (PR)] processor decompresses the data only in the data fields preceded by an indication of a compressed state.

7. (Amended) A protocol data unit [or [being received notably by the chip card according to any one of Claims 1 to 6] a chip card that receives compressed data fields, comprising a header [(ET)] and a data field [(DATA)], the header including the length [(LC)] of the data field], characterised in that the header (ET) comprises and an indication [(LDD) on] of the expected decompressed data length after decompression of the data field [(DATA)]

8. (Amended) A protocol data unit according to Claim 7, in which the indication [on] of the expected decompressed data length [(LDD)] is an n-bit word equal to the expected length of the decompressed data modulo 2^n , the expected length being

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expressed in decompressed m-bit data words, with n being an integer equal to at least 0 and m being an integer equal to at least 1.

9. (Amended) A protocol data unit according to Claim 7 [or 8], in which the header [(ET)] comprises further includes the number [(ALi)] of a decompression algorithm by means of which the data compressed in the data field [(DATA)] are] is to be decompressed.

10. (Amended) A protocol data unit according to Claim 9, in which the header [(ET)] comprises further includes the number [(Mij)] of a decompression model which corresponds to the decompression algorithm whose number [(ALi)] is included in the header and by means of which the compressed data in the data field [(DATA)] are] is to be decompressed.

11. (Amended) A protocol data unit according to [any one of Claims 7 to 10] Claim 7, in which the header [(ET)] includes a data state indication [(B1, B2)] having a first state [(DATA)] when the data in the data field are not compressed, and having a second state when the data in the data field [(DATA)] are compressed.

12. (Amended) A protocol data unit according to Claim 11, in which the data state indication [(B1, B2)] has a third state when the data in the data field [(DATA)] are to

be decompressed in accordance with a predetermined decompression algorithm [(AL0)] and a predetermined decompression model [(M00)].

13. (Amended) A protocol data unit according to Claim 11 [or 12], in which the header [(ET)] comprises the number of a decompression algorithm [(ALi)], the number of a decompression model [(Mij)] and the indication [on] of the expected decompressed data length [(LDD)] when the data state indication [(B1, B2)] is at the second state.

14. (Amended) A method for decompressing compressed data fields [(DATA)] to be implemented notably in the chip card according to any one of Claims 1 to 6] in a chip card, each compressed data field being preceded by an indication [on] of the expected decompressed data length [(LDD)] corresponding to compressed data contained in the field and by a length [(LC)] of the compressed data contained in the field, [characterised by] comprising the following steps:

- detecting [(D3)] the length [(LC)] of the compressed data and storing [(D0)] the compressed data field [(DATA)] over the detected length, and
- detecting [(D8)] the indication [on] of the expected decompressed data length [(LDD)] and decompressing [(D9)] the data so as to stop the decompression according to the detected indication [(LDD)].

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15. (Amended) A method according to Claim 14, [comprising a step (D6)]
~~further including the steps~~ of selecting a decompression algorithm [(A1*i*)] amongst several
 decompression algorithms [(A10 to A1*i*)] according to the number of an algorithm
 preceding the compressed data field [in order to decompress], ~~and decompressing the data~~
 in the field in accordance with the selected decompression algorithm.

16. (Amended) A method according to Claim 15, [comprising a step (D7, D72)]
~~further including the steps~~ of selecting a decompression model [(M1*j*)] amongst several
 decompression models [(M10 to M1*j*)] associated with the ~~selected~~ decompression algorithm
 [selected] according to the number of a model preceding the compressed data field [in order
 to decompress], ~~and compressing the data in the field in accordance with the decompression~~
 algorithm selected and the decompression model selected.

17. (Amended) A method according to Claim 15, [comprising a step (D71)]
~~further including the steps~~ of storing a decompression model [(M1*j*)] received previously in
 the compressed data field [(DATA)] in order to decompress], ~~and decompressing the data in~~
 the field in accordance with the selected decompression algorithm and the stored
 decompression model.

18. (Amended) A method according to Claim 15, [comprising a step (D71)]
~~further including the steps~~ of storing a decompression model [(M1*j*)] deduced implicitly
 from the received compressed data field [(DATA)] in order to decompress], ~~and~~
~~decompressing the data in the field in accordance with the selected decompression~~
 algorithm and the deduced and stored decompression model.

19. (Amended) A method according to [any one of Claims 14 to 18, comprising
 a step (D4, D50, D51, D52)] Claim 14, ~~further including the step~~ of detecting a data state
 indication [(B2, B3)] preceding each [decompressed] ~~received~~ data field in order to
 decompress the data in the field only when the data state indication is not at a first
 predetermined state.

20. (Amended) A method according to Claim 19, [according to which] ~~wherein~~
 the step [(D8)] of detecting an indication [on] of the expected decompressed data length
 [(L10)] is not performed when the data state indication [(B2, B3)] is at a predetermined
 state indicating that the compressed data are to be decompressed according to a
 predetermined algorithm and model [(A10, M10)].